A Complete Method for Porting Operating System for Embedded Systems

Osvaldo de Souza, Helano S. Castro

LESC – Laboratório de Engenharia de Sistemas de Computação
DETI - Depto Engenharia de Teleinformática – Universidade Federal do Ceará (UFC)
Campus do PICI bloco 910 – Fortaleza – CE – Brazil
{osvaldo,helano}@lesc.ufc.br

Abstract. Embedded system development frequently uses the “trial and error” approach for Operating System (OS) porting, resulting in incomplete or inconsistent porting results. In this paper, we present an original work addressing this issue. We propose a complete method for detecting OS parts that should be adjusted in order to port the OS into a new hardware platform. The proposed method combines information from the OS source-code and peculiarities of the new hardware platform, resulting in: a complete list of source-codes that must be adjusted; the interdependence between these source-codes; the priority order of modifications for each source-code; and an effort-based schedule, in order to plan the modifications.

1. Introduction

Most methods and tools used in Embedded Systems (ES) development have the hardware elements as their main focus. This can be partially explained by observing that usually ES are developed by engineers with small computer science knowledge [LEE 2000]. Concerning the software elements of a particular project, those related to high-level applications consume most of the development efforts, while Operating System (OS) has minimal attention and frequently it does not figure as a priority item. Actually, OS adjustment efforts rarely are not improvised. Recent researches have proposed development models where hardware and software development is carried out in parallel. However, they do not provide any OS development methods and/or specific-adjustment approach [POSADAS 2004][CARRO 2004]. Despite OS is a significant subject in computer science, when it comes to its architecture and design, there is no specific approach which deals with OS development or adjustments. This may be problematic, especially in ES development, because the source-code must be constantly modified in order to meet new hardware requirements. Most OS modifications are based on a “trial and error” approach and obviously, this is not the best way to deal with this problem. While there are generic development methods, which are applicable for OS development, a formal approach is necessary in order to investigate if a general methodology could be derived from these generic methods. As far as we know, up to present there are no appropriate methods for OS porting in the literature (which was clear for the extremely reduced number of papers on the subject) [FROHICH 2001]. The greatest motivation for this research was the realization that (after having designing many embedded systems) there is no methodology for porting OS for ES. This paper
describes a complete method designed to do that job (another paper containing a case using our algorithms is scheduled to be published very soon).

### 1.1. Software Development model

There are some methods that were conceived having in mind general development but they are applicable for OS development. Unfortunately, such methods did not take into consideration OS porting into a new hardware. However it is worthwhile mentioning them, since they can give us an insight into how to cover that gap. We present below a short description of those software development models applicable to OS:

**Application-Oriented Model.** According to this model, an OS shall be defined only for its respective high-level functionalities, for which the OS provides all required runtime support [BOOCH 2006]. This paradigm aims at maximizing all software elements in order to enhance a specific application. This is not a generalist approach, so it can not be applied to general purpose OS development. Moreover, it is important to observe that this paradigm does not supply a specific technique for OS porting.

**Object-Oriented Model (OO).** The OO concept consists in a development method in which system decomposition is based on objects idea. Applying OO to OS development has been considered, for example, as in the OS development called CHOICES, carried out at the Illinois University [CAMPBELL 1987].

OO languages, like C++, are useful for OS development, but the OO paradigm does not provide specific support for porting OS [FROHICH, 2001] [POLPETA 2005].

**Family-Oriented Model.** In this case, a set of programs is considered to be part of a family if they share sufficiently common features to the point that it is more useful studying their common features than their differences [PARNAS 1976] [POLPETA 2005]. For instance, the source-code set involved in task-control is usually called *task manager*. A great deal of concepts derived from this model is present in modern OS designs (e.g. Linux). However, this paradigm only provides support for OS design and development, and it does not address how to port an OS into a new platform.

### 1.2. General ES Development Model

Software development models are not designed to provide support for OS modifications; hence, ES development does not possess strong models to deal with OS porting. This is a strong reason for studying general models for ES development, and so we did. As a result of this study, we were able to identify some major steps that should be taken in order to develop an ES project. These steps are listed in Table 1. Note that the problem of selecting an ES OS only appears in step N5, after the hardware prototype is finished. Unfortunately, step N5 is supported only by a small set of tools.

**Table 1. General Embedded System Development Steps**

<table>
<thead>
<tr>
<th>Step</th>
<th>Description</th>
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<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>N0</td>
<td>High level functional specification level</td>
<td>N4</td>
<td>Prototype building</td>
</tr>
<tr>
<td>N1</td>
<td>Main hardware’s elements selections</td>
<td>N5</td>
<td>OS selection and adjustments</td>
</tr>
<tr>
<td>N2</td>
<td>Possible OS selection</td>
<td>N6</td>
<td>High level applications design</td>
</tr>
<tr>
<td>N3</td>
<td>Hardware platform design</td>
<td>N7</td>
<td>Tests</td>
</tr>
</tbody>
</table>

Many ES development projects do not have enough planning efforts, concerning OS porting. During the project execution, activities required for each step have
relationships which impact over other activities and steps. These relationships are called transitions. All transitions for a general ES development project are showed in Table 2. For instance, T0-1 means the transitions between steps 0 and 1. Figure 1 shows a graph which represents all transitions and steps obtained from the combination of Table 1 and Table 2.

Table 2. Transitions for Table 1

<table>
<thead>
<tr>
<th>Transition</th>
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</tr>
</thead>
<tbody>
<tr>
<td>T0-1</td>
<td>Impact of functional-specification over hardware</td>
<td>T5-4</td>
<td>Target-platform choice</td>
</tr>
<tr>
<td></td>
<td>specification</td>
<td></td>
<td></td>
</tr>
<tr>
<td>T1-0</td>
<td>Impact of hardware-specification over functional-</td>
<td>T5-5</td>
<td>Modifications on target</td>
</tr>
<tr>
<td></td>
<td>specification</td>
<td></td>
<td>OS</td>
</tr>
<tr>
<td>T1-2</td>
<td>OS hardware-support analysis for acceptance-</td>
<td>T5-6</td>
<td>Hardware and software</td>
</tr>
<tr>
<td></td>
<td>conditions evaluation</td>
<td></td>
<td>platforms available</td>
</tr>
<tr>
<td>T1-3</td>
<td>Hardware-platform design based on functional-</td>
<td>T6-7</td>
<td>Applications tests and</td>
</tr>
<tr>
<td></td>
<td>specifications</td>
<td></td>
<td>validations</td>
</tr>
<tr>
<td>T3-4</td>
<td>Hardware-prototype building</td>
<td>T7-7</td>
<td>Applications adjustments</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>and improvements</td>
</tr>
<tr>
<td>T4-5</td>
<td>“Trial and error” OS activations in hardware-</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>prototype</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

It is noteworthy that all activities related with OS modifications are grouped in step N5’s transitions.

Figure 1. Transitions Graph and Steps

Modifications and adaptations on the project that occur in step N5 do not follow any specific method, because there is no method which reveals all required modifications. It is possible that some of these modifications may not be completed when they are necessary or they were simply forgotten, resulting an incomplete or inconsistent porting. In addition, there is no plan or schedule guiding the development simply because all the required modifications are unknown.

2. The Proposed Method

Considering that there are no well-defined methods supporting the related activities for OS porting, we proposed a new method that discloses all source-code that must be analyzed, modified, reduced or increased in order to port an OS. The proposed method combines information obtained from the OS source-code and from the new hardware platform’s particularities, and it is based on source-code cross reference. The resulting cross reference information shows all the required source-code to provide software-support to the hardware. A table holding the source-code relationship, called crossover table, must be created when executing the crossover step. Creating such table requires expert knowledge about the OS and the target hardware, in order to bond the software and the respective hardware it must provide support for.
2.1. Method Overview

Table 3 shows the steps of the proposed method. They are enumerated from 0 to 4. Table 4 shows the transitions for these steps. Eventually, any of these steps can be re-run during the project execution.

<table>
<thead>
<tr>
<th>Step</th>
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</tr>
</thead>
<tbody>
<tr>
<td>N0</td>
<td>Evaluate Requirements through Decision Making Support</td>
<td>N3</td>
<td>Identify all source-code relationships</td>
</tr>
<tr>
<td>N1</td>
<td>Identify Hardware elements without software support</td>
<td>N4</td>
<td>Identify the source-code precedence</td>
</tr>
<tr>
<td>N2</td>
<td>Identify all source-code to be modified</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

Table 4. Transitions for the Proposed Method

<table>
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<tbody>
<tr>
<td>T0-0</td>
<td>Evaluate Requirements through decision making support</td>
</tr>
<tr>
<td>T0-1</td>
<td>Impact of hardware-elements over software elements</td>
</tr>
<tr>
<td>T1-0</td>
<td>Reevaluation of hardware-elements impact over the decision making support</td>
</tr>
<tr>
<td>T2-0</td>
<td>Impact of software-elements over project efforts, required for project accomplishment</td>
</tr>
<tr>
<td>T2-1</td>
<td>Reevaluation of software-elements impact over the decision making support</td>
</tr>
<tr>
<td>T2-2</td>
<td>Impact of software availability over hardware definition</td>
</tr>
<tr>
<td>T1-2</td>
<td>Impact of hardware availability over software definition</td>
</tr>
<tr>
<td>T2-3</td>
<td>Impact of new software-elements over the dependency table</td>
</tr>
<tr>
<td>T3-3</td>
<td>Recursive definition of software-dependencies</td>
</tr>
<tr>
<td>T3-4</td>
<td>Software-dependencies impact over precedence table actualization</td>
</tr>
<tr>
<td>T4-3</td>
<td>Precedence table impact over the dependency table – Creating precedence table</td>
</tr>
</tbody>
</table>

Figure 2 shows a graph representing all transitions and steps regarding the combination of Table 3 and Table 4. Steps N0, N1 and N2 are strongly connected and most critical issues are addressed in their transitions. A detailed description of all steps is presented in the next sections.

Figure 2. Five Steps Method Graph

2.1.1. Step N0 – Evaluate Requirements through Decision Making Support

The fundamental goal of step N0 is to submit the initial decision to the use of a Decision Making Model (DM) [MARKO 2001]. Figure 3 shows a suitable DM for a ES development project.
During project execution, DM can be adjusted in order to include new constraints. Of course, a suitable DM helps decreasing the effort placed on critical project’s phases [MARKO 2001]. The DM recursively uses other steps from the proposed method in order to provide the following information: if the target OS is suitable for the target hardware-platform; all hardware-elements lacking OS support; and all software-elements needing modifications for the new hardware-platform.

2.1.2. Step N1 – Identify Hardware-Elements without Software Support

Step N1 aims at identifying discrepancies in the target hardware platform for the chosen OS. The crossover table is the main result obtained from this step. If a different hardware element is present in the new hardware-platform, then a similar\(^1\) element must be selected. If the different hardware does not have any similar element supported in the chosen OS, the way to keep them working is by developing and using device drivers in order to provide OS supporting.

2.1.3. Step N2 – Identify all source-code to be modified

The precise definition of all source-code to be modified requires an extension of the crossover table, which takes place in steps N1 and N2. Note that only hardware-dependent source-code must be considered. The table extension is obtained by applying the read-and-search approach over the source-code.

2.1.4. Steps N3 and N4

Three kinds of essential information are revealed through the crossover table: dependencies, requirements, and precedence. Dependency identifies the interconnection’s level of the source-code needing modification. Dependency’s level of a given source-code is obtained by adding all other source-codes from which the given source-code depends on. In general, the dependency computation is carried out as seen in (1) and the total of requirements as seen in (2).

---

\(^{1}\) Similar hardware means another hardware with the same functions
The work needed for obtaining the information related to dependency and requirements of a source-code is a read-and-search job inside the OS source-codes. In order to perform this work, it is important to use some application. An application which gathers all steps of the proposed method and produces all required information, including crossover, was developed as a study case. However, tools as Code Count, CallTree, Free Code Graphing Project, and Source Navigator can be of great help [LSE 2006] [Navigator 2006]. Special care must be taken when solving cyclic references between source-codes since they could result in inadequate precedence classification. Such a problem occurs when a source-code file uses services implemented on another source-code.

Cyclic references must be resolved before the precedence between routines is computed. A strategy for solving this problem consists on using the Mock Object Pattern for creating temporary replacement routines [BROWN 2003]. In order to distribute the large information volume to be manipulated when porting an OS, a template/model for crossover-reference table called Dependency Matrix is proposed in the following subsection.

2.1.4.1. Step N3 – Creating a Dependency Matrix

Figure 4 depicts a dependency matrix, proposed for ES development. All source-code (subcomponents) and source-code families (components) that must be modified are placed in this matrix. For the purpose of clearness, the example shown in Figure 4 only presents the subcomponent’s level. Again, it is important to observe that only the source-code to be ported must be referenced. The matrix is filled by marking a component that depends on other component. In order to do that, each subcomponent (in a row) is inspected by marking all cells in that row containing a subcomponent (in a column) from which the given subcomponent depends on.

\[
\text{Dep}(x) = \sum_{i=1}^{k} i \\
\begin{cases} 
1 & \text{if } x \text{ depends on } k_i \\
0 & \text{if } x \text{ does not depend}
\end{cases}
\tag{1}
\]

\[
\text{Req}(x) = \sum_{i=1}^{k} i \\
\begin{cases} 
1 & \text{if } x \text{ provides services for } k_i \\
0 & \text{if } x \text{ does not provide}
\end{cases}
\tag{2}
\]

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2.1.4.2. Step N4 – Creating a Precedence Matrix

A precedence matrix is obtained from information resulting from the processing of the dependency matrix. The Algorithm 1 (showed below) is part of the proposed method, and it classifies all routines held in the precedence matrix at the same time as it also solves cyclic reference issues.

This processing considers the dependencies and requirements computed using (1) and (2), respectively. Once the dep(s) and req(s) values are defined, all information needed in order to provide data required in steps 1, 2 and 3 of Algorithm 1 should be available.

Figure 5 contains an example precedence matrix that is obtained by adding the effort needed for each adjustment to the result from Algorithm.

```
1. For each routine, all of dependencies of the routine are registered in a list.
2. Let DEP(i)= total of dependencies of the given routine
3. Let REQ(i)= total of requirements of the given routine
4. Make SUP(i) = 0
5. Make PEND(i) = DEP(i)
6. Make PRIORI(i) = 0
7. Make PRIORITY = 1
8. The dependency list is classified in ascending order based on PEND and in descending order based on REQ
9. For each routine pair R(i) and R(k) com PEND(i)>0, and cyclic dependence::
   1. A mock routine named mock(i)(k) is created
   2. Make REQ(mock(i)(k))=1: Make DEP(mock(i)(k))=0: Make SUP(mock(i)(k))=0
   3. Add mock(i)(k) to the dependencies of R(i)
   4. Remove R(k) from the dependencies of R(i)
10. Classify the dependency list in ascending order based on PEND and descending order based on REQ
11. For each routine R(i) such as PEND(i) = 0 and PRIORI(i) = 0, do:
   1. Make PRIORI(i) = PRIORITY: PRIORITY = PRIORITY + 1
   2. For each routine R(k) depending on R(i), do:
      1. SUP(k)= SUP(k)+1
      2. PEND(k)= DEP(k) − SUP(k)
12. Repeat step 10 until all routines are prioritized
13. Classify the dependency list in ascending order by PRIORI
```

Algorithm 1. Priority Computation and Cyclic Reference Removal.

3. Conclusions

The proposed method covers the essential steps for porting an OS into a new hardware platform. The method aggregates quality to the ES development projects by providing a complete and anticipated view of the development as well as the adjustment activities to be performed. As far as we know, this is an original work addressing this specific issue. Developing ES without a well-defined approach may result in unstable or incomplete
projects. The proposed approach for obtaining dependencies between source-codes comprising the OS assures that all adjustments are considered.

The proposed algorithm also resolves problems arising from cyclic references, which are a real challenge in ES projects. The proposed table and matrix templates, that result from the algorithm application, allows for fast visual identification of the entire demanded work. It is important to observe that the precedence matrix provides not only information for determining the correct order of alterations, but also valuable management information for tracking the effort spent in the project.

The proposed method goes beyond its main goal of showing which adjustments must be done, and it also provides levels of control, priority and predictability over the ES development project in which porting of an OS is a fundamental task.

4. References

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